

A New FPGA Prototype for Synchro to Digital Converter Using CORDIC Algorithm

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Abstract— This paper introduce a new approach to FPGA implementation of synchro to digital converter using Co-ordinate Rotation Digital Computer CORDIC algorithm. This algorithm was the best replacement of analog conversion system by the high resolution digital. CORDIC algorithm used for the fast calculation of elementary functions like multiplication, division, trigonometric functions. The approach is based on receiving the synchro signals S_1 , S_2 , and S_3 from the synchro motor, and converts them to two perpendicular signals sin signal and cosine signal using solid state Scott-t transformer. Then the a CORDIC circuit receive this signals after converting them to digital and produce the azimuth angle in digital format. This prototype of hardware implementation of CORDIC algorithm used Spartan-III series FPGA, with constraint to area efficiency and throughput architecture. The prototype results show that the conversion time is less than $1\mu s$ which is suitable for real time applications in radar and missile control applications.

1. INTRODUCTION

CORDIC is the abbreviation of Co-ordinate Rotation Digital Computer. The first description for iterative approach of this algorithm is firstly provided by Jack E. Volder in 1959 [1]. CORDIC algorithm provides an efficient way of rotating the vectors in a plane by simple shift add operation to estimate the basic elementary functions like trigonometric operations, multiplication, division and some other operations like logarithmic functions, square roots and exponential functions. Most of the applications either in wireless communication or in digital signal processing are based on microprocessors which make use of a single instruction and a bunch of addressing modes for their working. As these processors are costs efficient and offer extreme flexibility but yet are not suited for some of these applications. During the last 50 years the CORDIC algorithm has emerged in a wide variety of applications. The CORDIC algorithm has received increased attention after a unified approach is proposed for its implementation [2].

The CORDIC arithmetic processor chip is designed and implemented to perform various functions possible in rotation and vectoring mode of circular, linear, and hyperbolic coordinate systems [3]. Since then, CORDIC technique has been used in many applications [4], such as single chip CORDIC processor for DSP applications [5, 6]. Recently several researches applied CORDIC algorithm in radar pulse compression, rotary encoders, and waveform generation [7–12]. This paper implement CORDIC S/D converter to convert synchro signals into high resolution digital azimuth angle. This achieves a high precision S/D conversion with low propagation delay compared with that implemented using microcontroller or analog processing.

2. CORDIC ALGORITHM

The CORDIC algorithm involves rotation of a vector v on the XY -plane in circular, linear and hyperbolic coordinate systems depending on the function to be evaluated. The CORDIC algorithm performs a planar rotation. Graphically, planar rotation means transforming a vector (X_i, Y_i) into a new vector (X_j, Y_j) [13, 14].

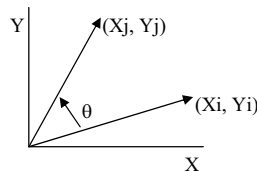


Figure 1: Rotating the vector.

Using a matrix form, a planar rotation for a vector of (X_i, Y_i) is defined as

$$\begin{bmatrix} X_j \\ Y_j \end{bmatrix} = \begin{bmatrix} \cos \theta & -\sin \theta \\ \sin \theta & \cos \theta \end{bmatrix} \begin{bmatrix} X_i \\ Y_i \end{bmatrix} \quad (1)$$

The θ angle rotation can be executed in several steps, using an iterative process. Each step completes a small part of the rotation. Many steps will compose one planar rotation. A single step is defined by the following equation:

$$\begin{bmatrix} X_{n+1} \\ Y_{n+1} \end{bmatrix} = \begin{bmatrix} \cos \theta_n & -\sin \theta_n \\ \sin \theta_n & \cos \theta_n \end{bmatrix} \begin{bmatrix} X_n \\ Y_n \end{bmatrix} \quad (2)$$

Equation (2) can be modified by eliminating the $\cos \theta_n$ factor.

$$\begin{bmatrix} X_{n+1} \\ Y_{n+1} \end{bmatrix} = \cos \theta_n \begin{bmatrix} 1 & -\tan \theta_n \\ \tan \theta_n & 1 \end{bmatrix} \begin{bmatrix} X_n \\ Y_n \end{bmatrix} \quad (3)$$

Equation (3) requires three multiplies, compared to the four needed in Equation (2). Additional multipliers can be eliminated by selecting the angle steps such that the tangent of a step is a power of 2. Multiplying or dividing by a power of 2 can be implemented using a simple shift operation. The angle for each step is given by

$$\theta_n = \arctan \left(\frac{1}{2^n} \right) \quad (4)$$

All iteration-angles summed must equal the rotation angle θ .

$$\sum_{n=0}^{\infty} S_n \theta_n = \theta \quad (5)$$

where

$$S_n = \{-1; +1\} \quad (6)$$

This results in the following equation for $\tan \theta_n$

$$\tan \theta_n = S_n 2^{-n} \quad (7)$$

Combining Equations (3) and (7) results in

$$\begin{bmatrix} X_{n+1} \\ Y_{n+1} \end{bmatrix} = \cos \theta_n \begin{bmatrix} 1 & -S_n 2^{-n} \\ S_n 2^{-n} & 1 \end{bmatrix} \begin{bmatrix} X_n \\ Y_n \end{bmatrix} \quad (8)$$

Besides for the $\cos \theta_n$ coefficient, the algorithm has been reduced to a few simple shifts and additions. The coefficient can be eliminated by pre-computing the final result. The first step is to rewrite the coefficient.

$$\cos \theta_n = \cos \left(\arctan \left(\frac{1}{2^n} \right) \right) \quad (9)$$

The second step is to compute Equation (9) for all values of ' n ' and multiplying the results, which we will refer to as K .

$$K = \frac{1}{P} = \prod_{n=0}^{\infty} \cos \left(\arctan \left(\frac{1}{2^n} \right) \right) \approx 0.607253 \quad (10)$$

K is constant for all initial vectors and for all values of the rotation angle, it is normally referred to as the congruence constant. The derivative P (approx. 1.64676) is defined here because it is also commonly used. We can now formulate the exact calculation the CORDIC performs.

$$\begin{cases} X_j = K (X_i \cos \theta - Y_i \sin \theta) \\ Y_j = K (Y_i \cos \theta + X_i \sin \theta) \end{cases} \quad (11)$$

Because the coefficient K is pre-computed and taken into account at a later stage, Equation (8) may be written as

$$\begin{bmatrix} X_{n+1} \\ Y_{n+1} \end{bmatrix} = \begin{bmatrix} 1 & -S_n 2^{-n} \\ S_n 2^{-n} & 1 \end{bmatrix} \begin{bmatrix} X_n \\ Y_n \end{bmatrix} \quad (12)$$

or as

$$\begin{cases} X_{n+1} = X_n - S_n 2^{-2n} Y_n \\ Y_{n+1} = Y_n + S_n 2^{-2n} X_n \end{cases} \quad (13)$$

At this point a new variable called ‘ Z ’ is introduced. Z represents the part of the angle θ which has not been rotated yet.

$$Z_{n+1} = \theta - \sum_{i=0}^n \theta_i \quad (14)$$

For every step of the rotation S_n is computed as a sign of Z_n .

$$S_n = \begin{cases} -1 & \text{if } Z_n < 0 \\ +1 & \text{if } Z_n \geq 0 \end{cases} \quad (15)$$

Combining Equations (5) and (15) results in a system which reduces the not rotated part of angle θ to zero.

Or in a program-like style:

```

For  $n = 0$  to [inf]
  If ( $Z(n) \geq 0$ ) then
     $Z(n+1) := Z(n) - \text{atan}(1/2^n)$ ;
  Else
     $Z(n+1) := Z(n) + \text{atan}(1/2^n)$ ;
  End if;
End for;
```

The $\text{atan}(1/2^i)$ is pre-calculated and stored in a table. [inf] is replaced with the required number of iterations, which is about 1 iteration per bit (16 iterations yield a 16bit result).

If we add the computation for X and Y we get the program-like style for the CORDIC core.

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For  $n = 0$  to [inf]
  If ( $Z(n) \geq 0$ ) then
     $X(n+1) := X(n) - (Yn/2^n)$ ;
     $Y(n+1) := Y(n) + (Xn/2^n)$ ;
     $Z(n+1) := Z(n) - \text{atan}(1/2^n)$ ;
  Else
     $X(n+1) := X(n) + (Yn/2^n)$ ;
     $Y(n+1) := Y(n) - (Xn/2^n)$ ;
     $Z(n+1) := Z(n) + \text{atan}(1/2^n)$ ;
  End if;
End for;
```

This algorithm is commonly referred to as driving Z to zero. The CORDIC core computes:

$$[X_j, Y_j, Z_j] = [P(X_i \cos(Z_i) - Y_i \sin(Z_i)), P(Y_i \cos(Z_i) + X_i \sin(Z_i)), 0].$$

There's a special case for driving Z to zero:

$$\begin{aligned} X_i &= \frac{1}{P} = K \approx 0.60725 \\ Y_i &= 0 \\ Z_i &= \theta \\ [X_j, Y_j, Z_j] &= [\cos \theta, \sin \theta, 0] \end{aligned}$$

Another scheme which is possible is driving Y to zero. The CORDIC core then computes:

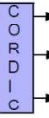
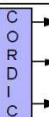
$$[X_j, Y_j, Z_j] = \left[P\sqrt{X_i^2 + Y_i^2}, 0, Z_i + \arctan\left(\frac{Y_i}{X_i}\right) \right]$$

For this scheme there are two special cases:

$$\begin{aligned}
 &1) \quad X_i = X \\
 &\quad Y_i = Y \\
 &\quad Z_i = 0 \\
 &[X_j, Y_j, Z_j] = \left[P\sqrt{X_i^2 + Y_i^2}, 0, \arctan\left(\frac{Y_i}{X_i}\right) \right] \\
 &2) \quad X_i = 1 \\
 &\quad Y_i = a \\
 &\quad Z_i = 0 \\
 &[X_j, Y_j, Z_j] = \left[P\sqrt{1 + a^2}, 0, \arctan(a) \right]
 \end{aligned}$$

As a Summary of CORDIC Functions is illustrated in Table 1.

Table 1: Summary of CORDIC algorithm.

	Rotation Mode: $d_i = \text{sign}(z^{(i)}); z^{(i)} \rightarrow 0$	Vectoring Mode: $d_i = -\text{sign}(x^{(i)}y^{(i)}); y^{(i)} \rightarrow 0$
Circular $\mu = 1$ $\alpha^{(i)} = \tan^{-1}2^{-i}$	 $x \rightarrow K(x.\cos z - y.\sin z)$ $y \rightarrow K(y.\cos z + x.\sin z)$ $z \rightarrow 0$ For $\cos z$ & $\sin z$, set $x = 1/K, y = 0$	 $x \rightarrow K(x^2 + y^2)^{1/2}$ $y \rightarrow 0$ $z \rightarrow z + \tan^{-1}(y/x)$ For $\tan^{-1} y$, set $x = 1, z = 0$
Linear $\mu = 0$ $\alpha^{(i)} = 2^{-i}$	 $x \rightarrow x$ $y \rightarrow y + (x.z)$ $z \rightarrow 0$ For multiplication, set $y = 0$	 $x \rightarrow x$ $y \rightarrow 0$ $z \rightarrow z + (y/x)$ For division, set $z = 0$
Hyperbolic $\mu = -1$ $\alpha^{(i)} = \tanh^{-1}2^{-i}$	 $x \rightarrow K^*(x.\cosh z - y.\sinh z)$ $y \rightarrow K^*(y.\cosh z + x.\sinh z)$ $z \rightarrow 0$ For $\cosh z$ & $\sinh z$, set $x = 1/K^*, y = 0$	 $x \rightarrow K^*(x^2 - y^2)^{1/2}$ $y \rightarrow 0$ $z \rightarrow z + \tanh^{-1}(y/x)$ For $\tanh^{-1} y$, set $x = 1, z = 0$

3. THE PROPOSED ARCHITECTURE

The resolver is an electromagnetic rotational device that detects angular displacement; an equivalent electrical representation and diagram of typical output signal formats a resolver are shown in Fig. 2. An ac excitation signal applied to the primary is inductive coupled to the secondary. The transformation ratios are amplitude modulated by the sin and cosine of the angle of the rotor relative to the stator.

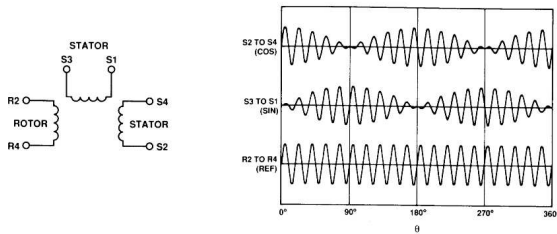


Figure 2: Electrical representation and typical resolver signals.

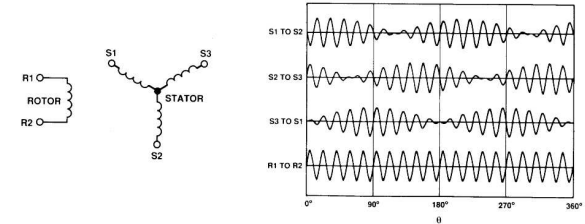


Figure 3: Electrical representation and typical synchro signals.

The operation of the synchro shown in Fig. 3 is very similar to that of the resolver. The fundamental difference is that the stator windings of the synchro are connected in a “Y” configuration, spaced 120 degrees apart, while the resolver has two isolated windings separated by 90 degrees.

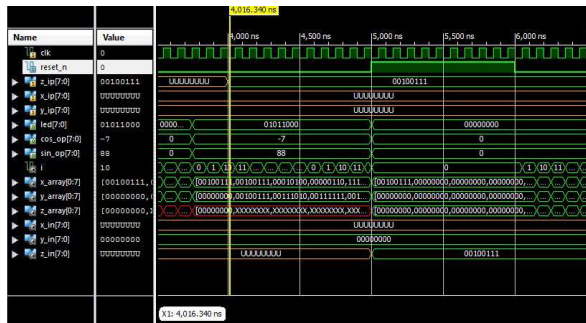


Figure 4: VHDL simulation of the CORDIC.

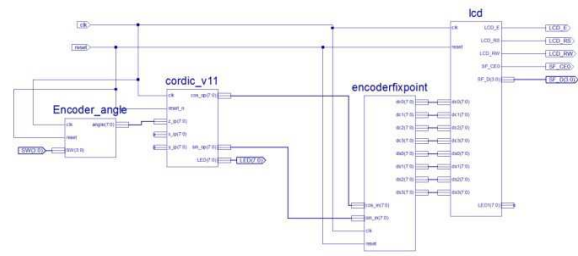


Figure 5: CORDIC Block diagram.



Figure 6: CORDIC FPGA Implementation.

The solid state Scott-T circuit uses two operational amplifiers to transform a synchro format signal into a resolver format. The envelop detector extract the synchro signals envelop and suppresses the references signal. The produced synchro signals are converted into digital format in order to be suitable for use with the FPGA. The CORDIC algorithm is implemented into the FPGA to produce the azimuth angle in digital format. The VHDL simulation is demonstrated in Fig. 4. The CORDIC block diagram is shown in Fig. 5.

4. CONCLUSION

This paper implement CORDIC S/D converter to convert synchro signals into high resolution digital azimuth angle. This achieves a high precision S/D conversion with low propagation delay compared with that implemented using microcontroller or analog processing. This prototype of hardware implementation of CORDIC algorithm used Spartan-III series FPGA, with constraint to area efficiency and throughput architecture. The prototype results show that the conversion time is less than 1 μ s which is suitable for real time applications in radar and missile control applications.

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